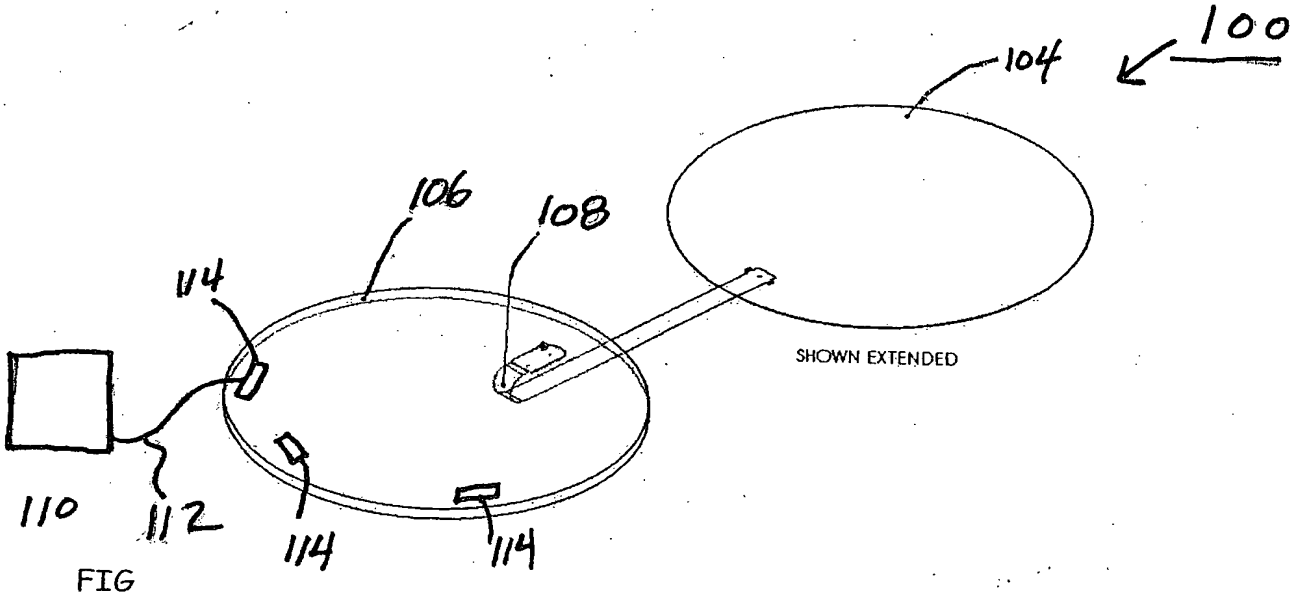
100
104
106
108
114
110
112
114
114
SHOWN EXTENDED

FIG 1A

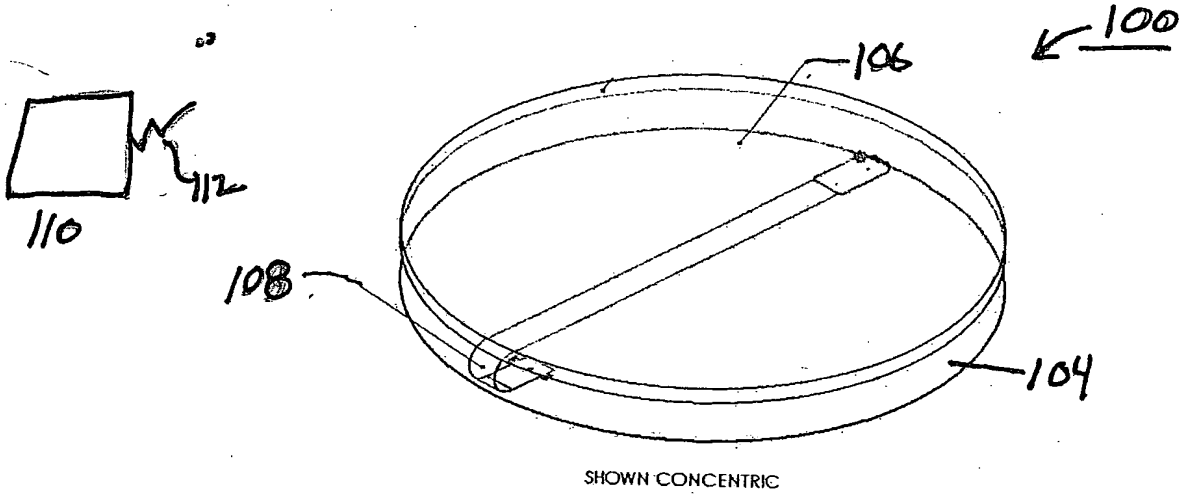
100
106
112
110
108
104
SHOWN CONCENTRIC

FIG 1B

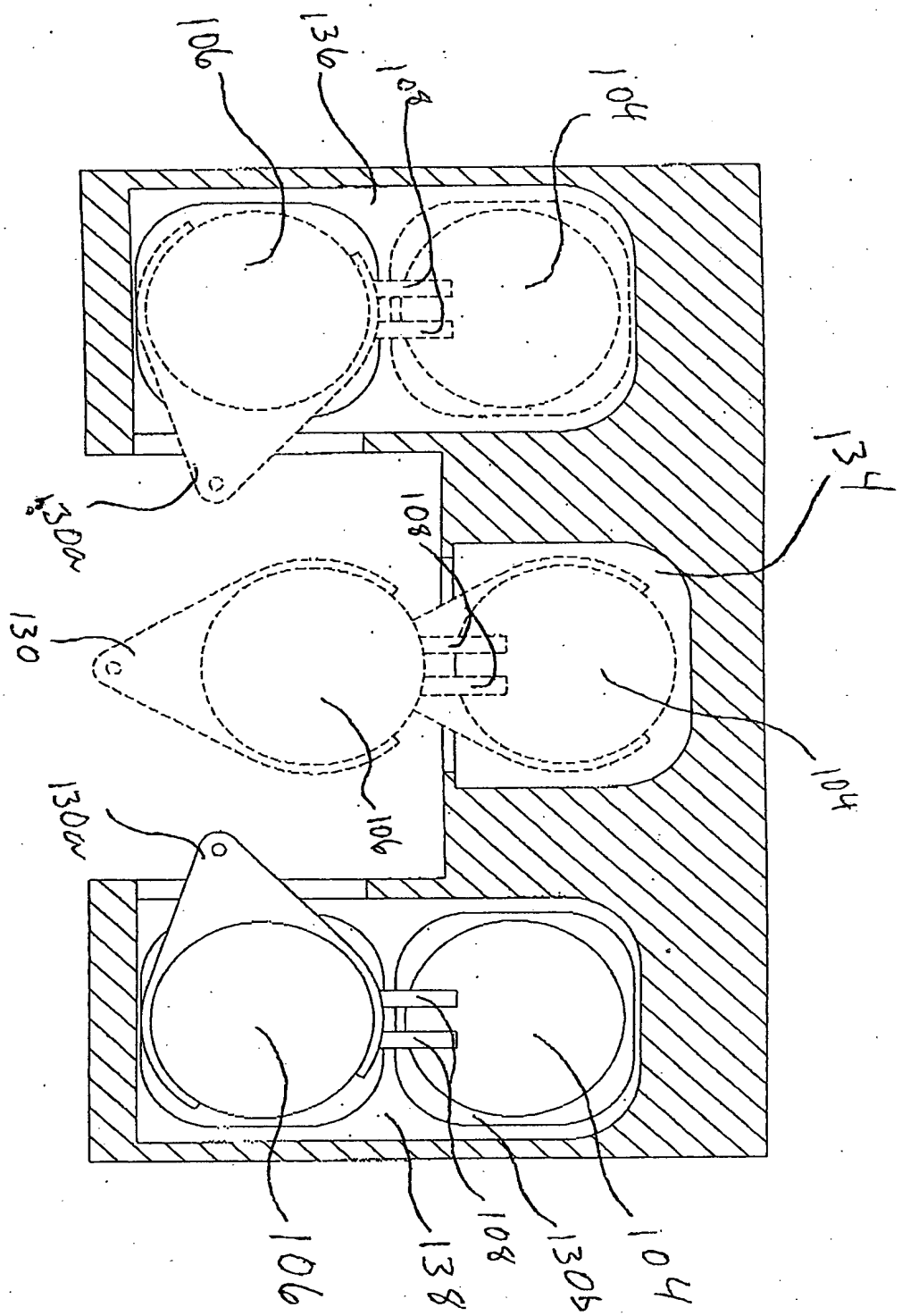
106
136
108
104
134
130a
108
130
104
106
130a
106
138
108
130b
104

FIG. 1C

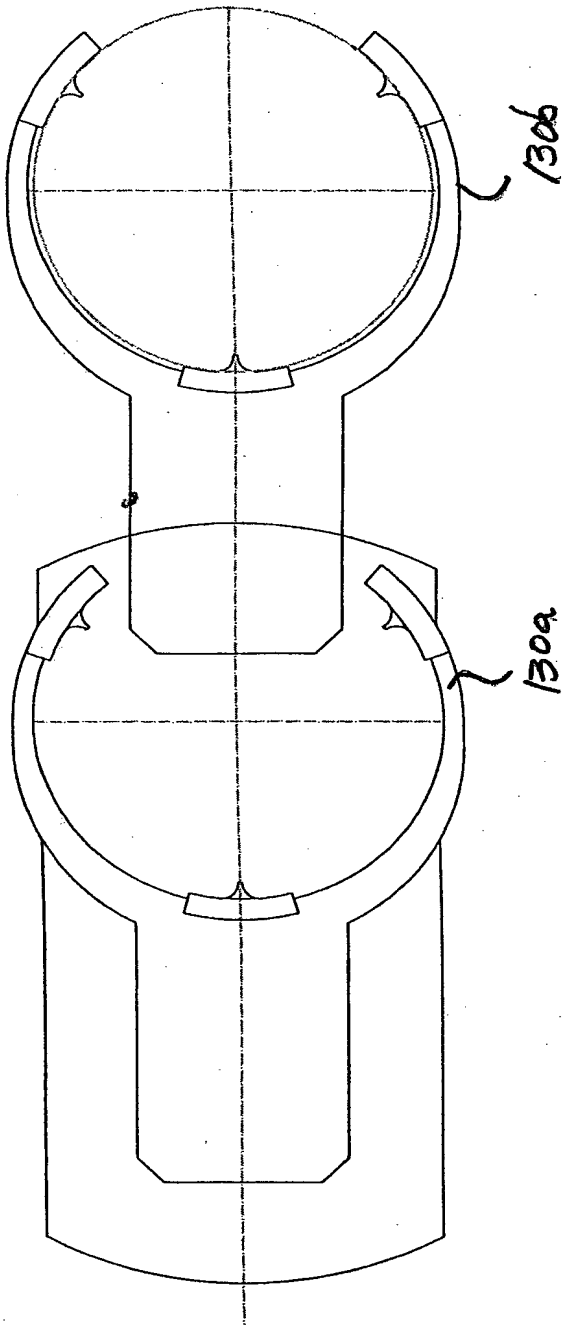

FIG 1D

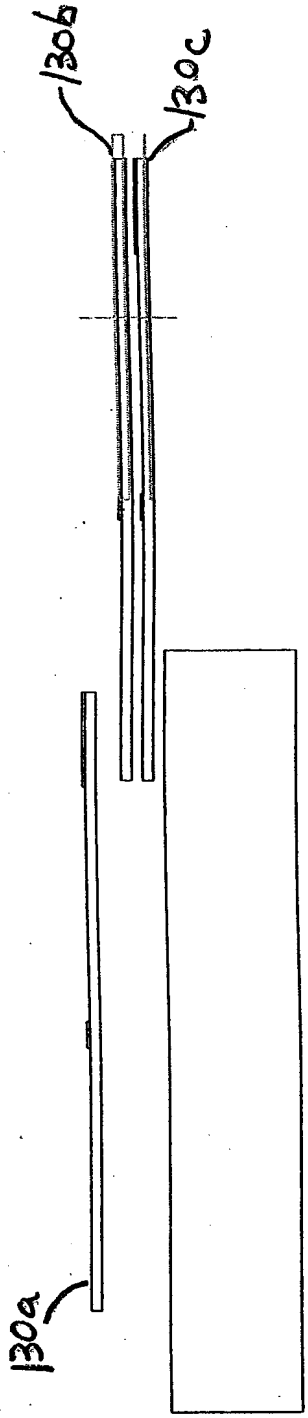

FIG 1E

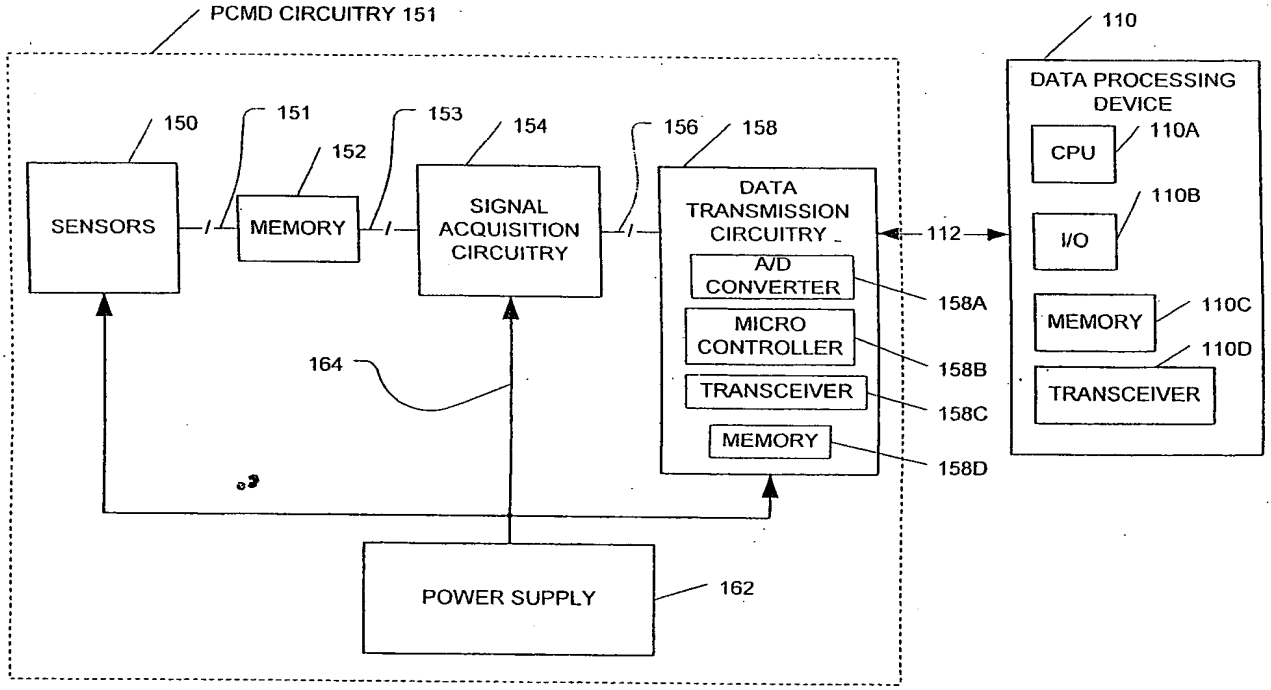
PCMD CIRCUITRY 151
110
DATA PROCESSING DEVICE
CPU
110A
I/O
110B
MEMORY
110C
TRANSCEIVER
110D
150
151
152
153
154
156
158
SENSORS
MEMORY
SIGNAL ACQUISITION CIRCUITRY
DATA TRANSMISSION CIRCUITRY
112
A/D CONVERTER
158A
MICRO CONTROLLER
158B
TRANSCEIVER
158C
MEMORY
158D
164
POWER SUPPLY
162

FIG 1F

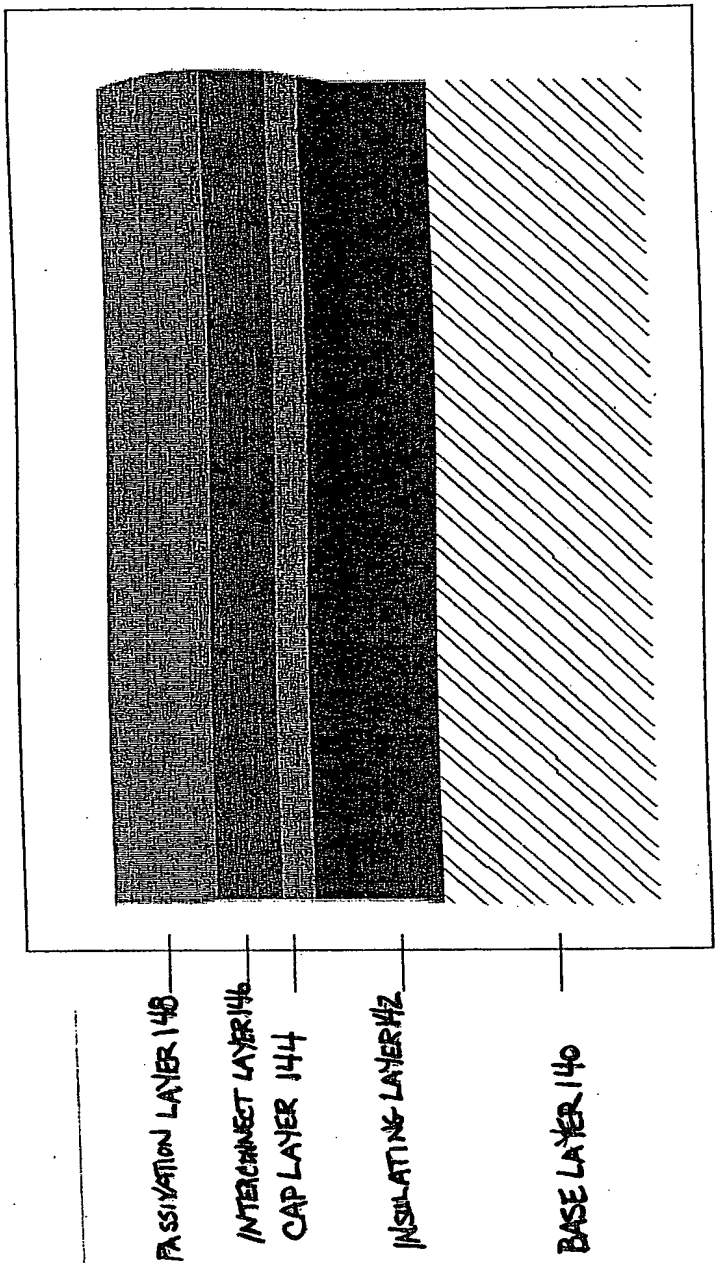
PASSIVATION LAYER 148
INTERCONNECT LAYER 146
CAP LAYER 144
INSULATING LAYER 142
BASE LAYER 140

FIG 1G

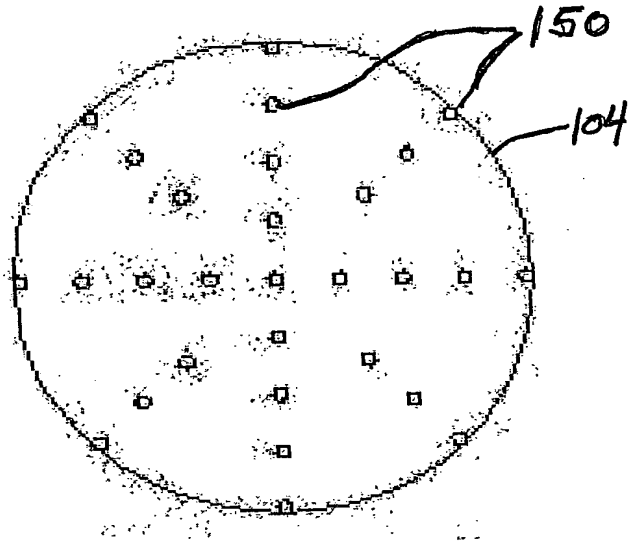

FIG. 1H

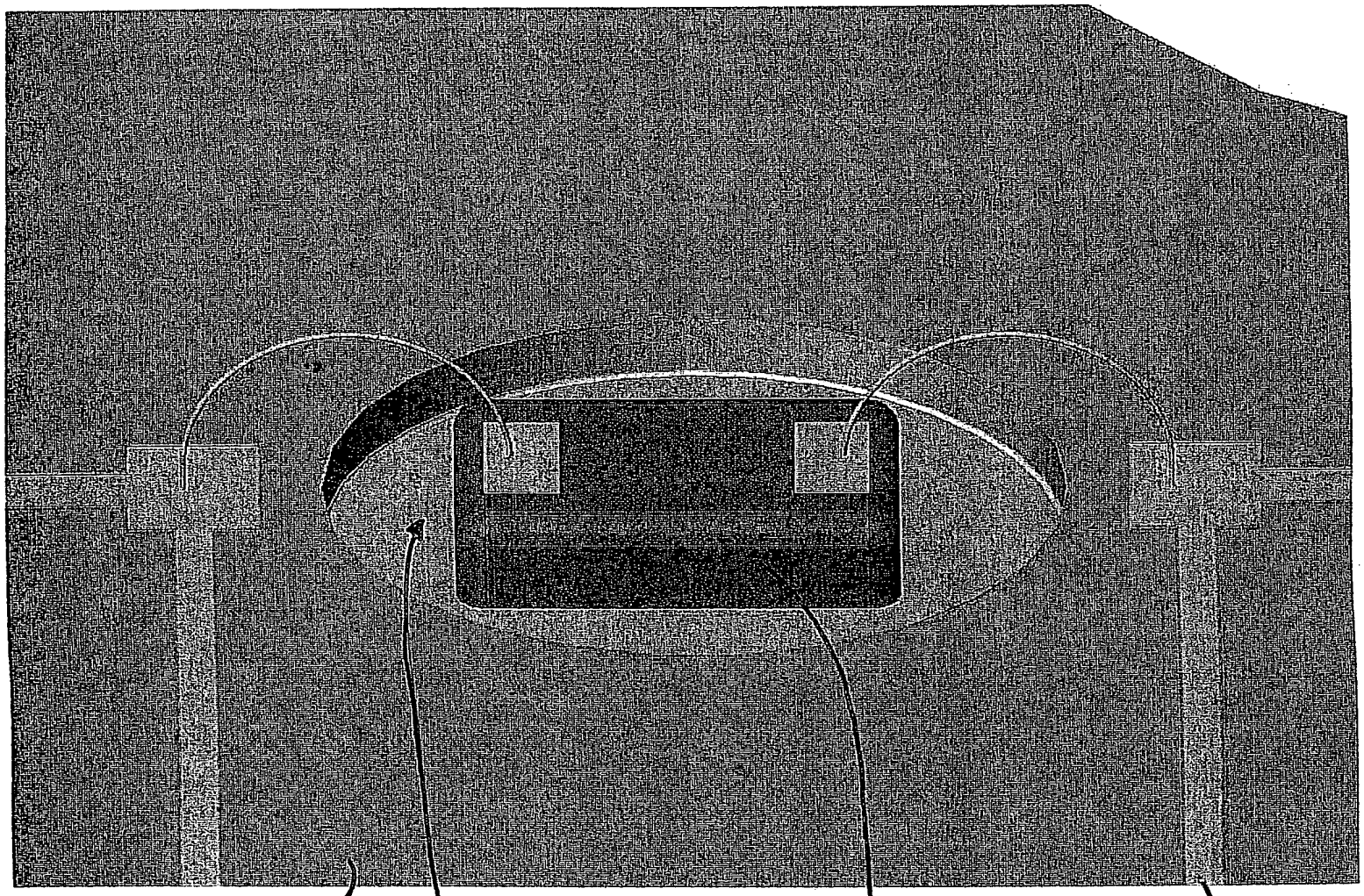

FIG 1J

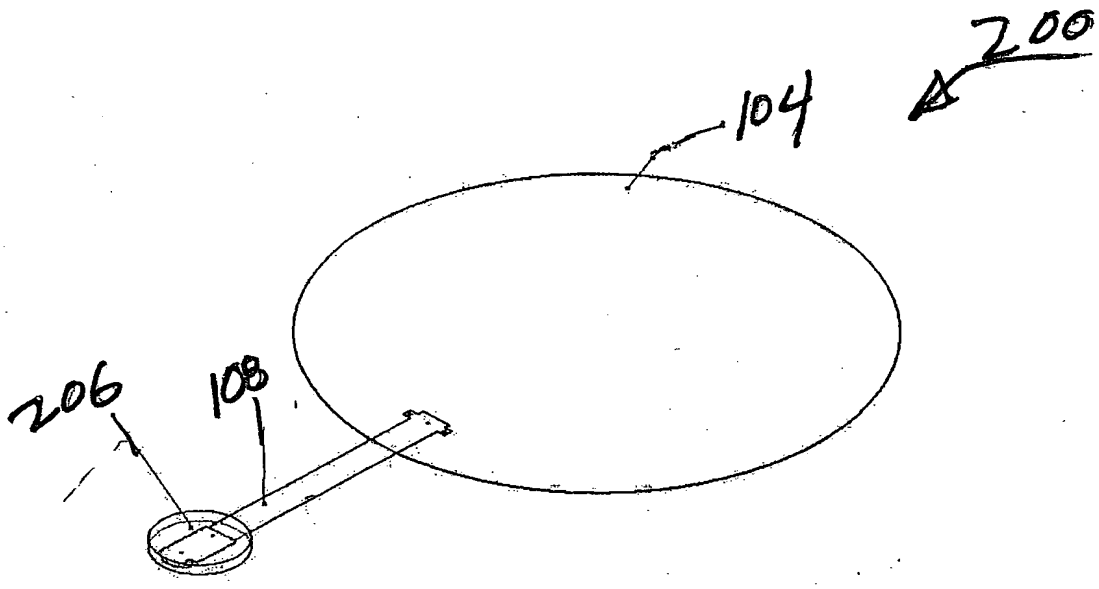

FIG 2

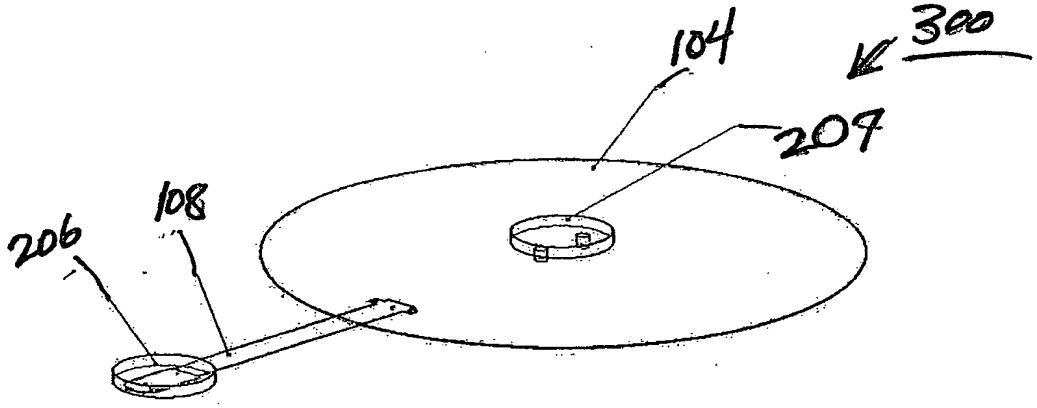

FIG 3A

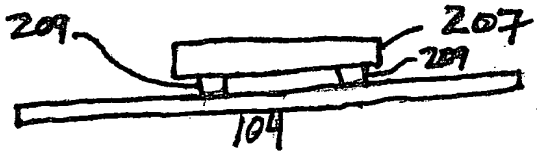

FIG. 3B

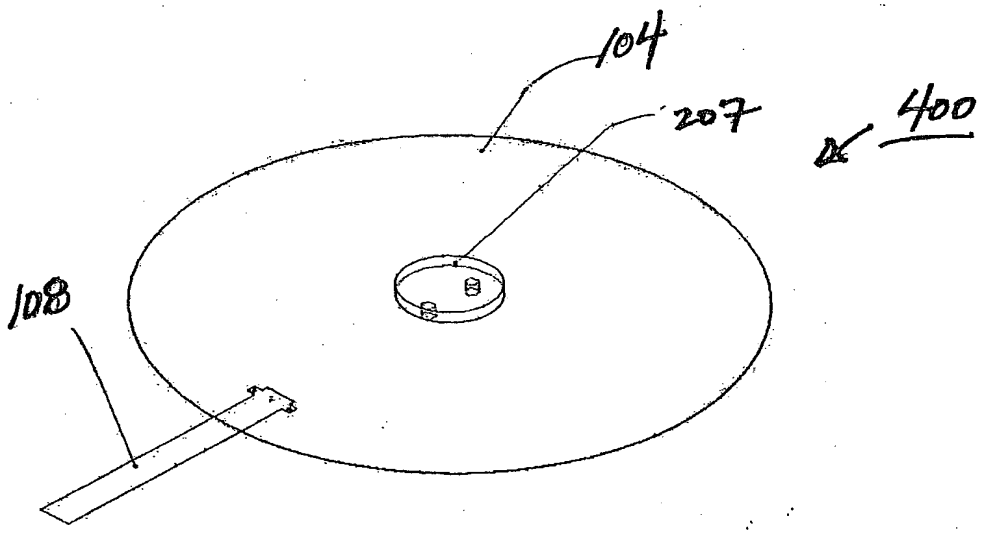

FIG 4

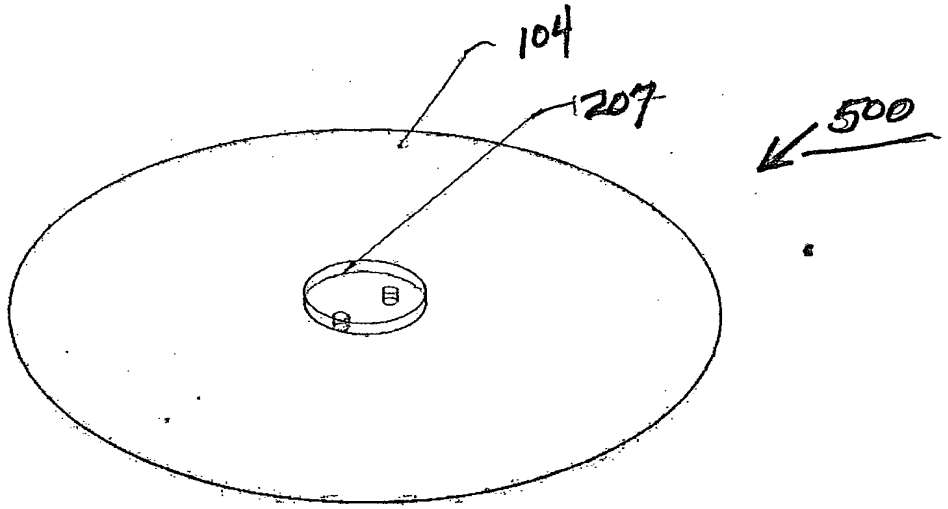

FIG 5

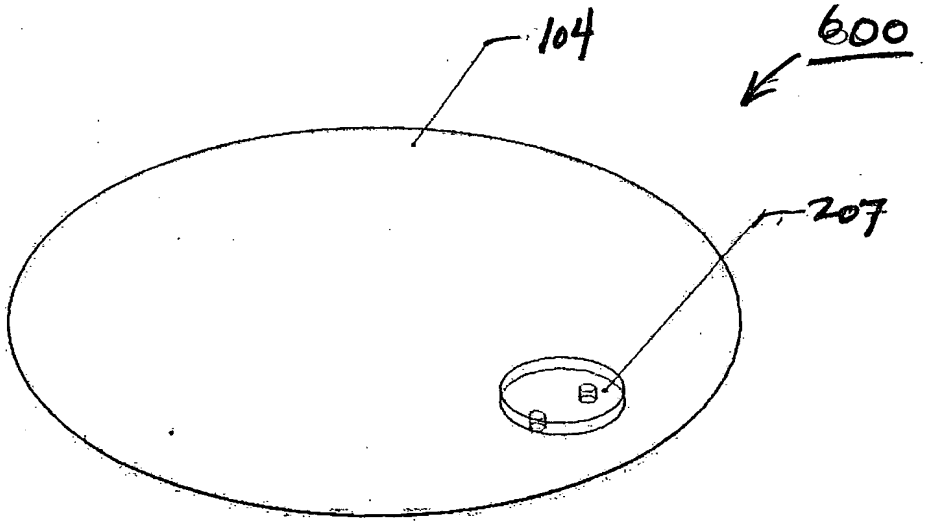

FIG6

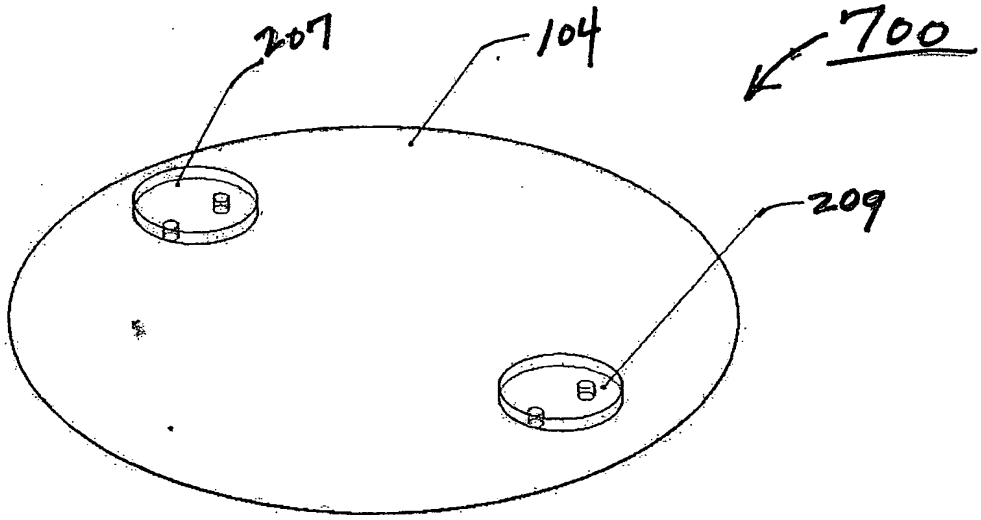

FIG 7